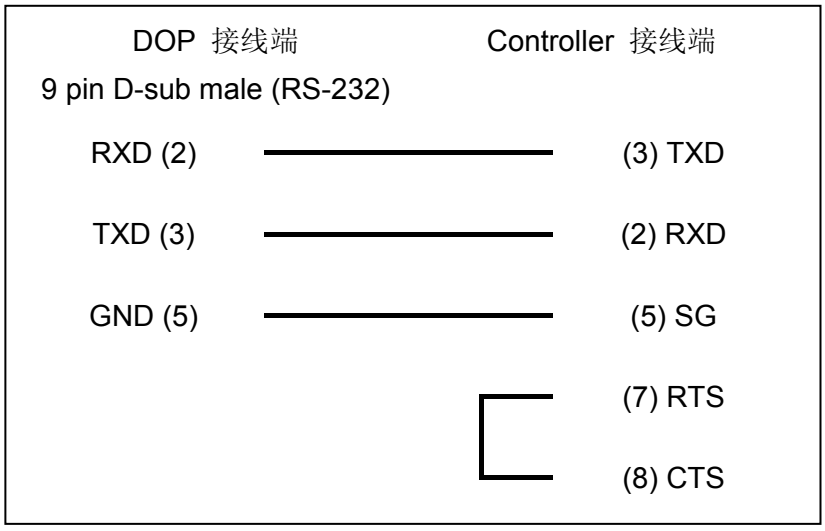
Allen-Bradley SLC5

人机默认值

通讯速率：19200, 8, None, 1
控制器站号：1
控制区/状态区：B3:0/B3:10

控制器接线的说明

a. RS-232（DOP-A/AE/AS, DOP-B 系列适用）



控制器 Read/Write 地址的定义

a. 寄存器

寄存器种类	符号格式 Word No. (n) Slot No. (s) File No. (f)	读写地址范围	数据长度	注
Output file	**O:**n	**O:**0 – **O:**255 (s = 0, f = 0)	Word	
	O:s.n	**O:**0.0 – **O:**255.255 (f = 0)		
Input file	**I:**n	**I:**0 – **I:**255 (s = 0, f = 1)	Word	
	I:s.n	**I:**0.0 – **I:**255.255 (f = 1)		
Status file	**S2:**n	**S2:**0 – **S2:**255 (f = 2)	Word	
Bit file	**B**f:n	**B**3:0 – **B**3:255, **B**9:0 – **B**255:255	Word	
Timer flag	**T**f:n	**T**4:0 – **T**4:255, **T**9:0 – **T**255:255	Word	

寄存器种类	符号格式 Word No. (n) Slot No. (s) File No. (f)	读写地址范围	数据长度	注
Timer Preset Value	**T**f:n.PRE	**T**4:0.PRE – **T**4:255.PRE, **T**9:0.PRE – **T**255:255.PRE	Word	
Timer Accumulator Value	**T**f:n.ACC	**T**4:0.ACC – **T**4:255.ACC, **T**9:0.ACC – **T**255:255.ACC		
Counter flag	**C**f:n	**C**5:0 – **C**5:255, **C**9:0 – **C**255:255	Word	
Counter Preset Value	**C**f:n.PRE	**C**5:0.PRE – **C**5:255.PRE, **C**9:0.PRE – **C**255:255.PRE		
Counter Accumulator Value	**C**f:n.ACC	**C**5:0.ACC – **C**5:255.ACC, **C**9:0.ACC – **C**255:255.ACC		
Control file	**R**f:n	**R**6:0 – **R**6:255, **R**9:0 – **R**255:255	Word	
Control Size of Bit Array	**R**f:n.LEN	**R**6:0.LEN – **R**6:255.LEN, **R**9:0.LEN – **R**255:255.LEN		
Control Reserved file	**R**f:n.POS	**R**6:0.POS – **R**6:255.POS, **R**9:0.POS – **R**255:255.POS		
Integer file	**N**f:n	**N**7:0 – **N**7:255, **N**9:0 – **N**255:255	Word	
Floating Point file	**F**f:n	**F**8:0 – **F**8:255, **F**9:0 – **F**255:255	Double Word	
String File	**ST**f:n	**ST**9:0 – **ST**255:255	41 Words	
Long Word File	**L**f:n	**L**9:0 – **L**255:255	Double Word	

b. 接点

接点种类	符号格式 Word No. (n) Slot No. (s) File No. (f) Bit No. (b)	读写地址范围	注
Output	**O:**n/b	**O:**0/0 – **O:**255/15 (s = 0, f = 0)	
	O:s.n/b	**O:**0.0/0 – **O:**255.255/15 (f = 0)	
Input	**I:**n/b	**I:**0/0 – **I:**255/15 (s = 0, f = 1)	
	I:s.n/b	**I:**0.0/0 – **I:**255.255/15 (f = 1)	
Status	**S2:**n/b	**S2:**0/0 – **S2:**255/15 (f = 2)	
Bit	**B**f:n/b	**B**3:0/0 – **B**3:255/15, **B**9:0/0 – **B**255:255/15	
Timer	**T**f:n/b	**T**4:0/0 – **T**4:255/15, **T**9:0/0 – **T**255:255/15	
	Tf:n/EN	**T**4:0/EN – **T**4:255/EN, (b = 15) **T**9:0/EN – **T**255:255/EN (b = 15)	

接点种类	符号格式 Word No. (n) Slot No. (s) File No. (f) Bit No. (b)	读写地址范围	注
Timer	**T**f:n/TT	**T**4:0/TT – **T**4:255/TT, (b = 14) **T**9:0/TT – **T**255:255/TT (b = 14)	
	Tf:n/DN	**T**4:0/TT – **T**4:255/TT, (b = 13) **T**9:0/TT – **T**255:255/TT (b = 13)	
Timer Preset Value	**T**f:n.PRE/b	**T**4:0.PRE/0 – **T**4:255.PRE/15, **T**9:0.PRE/0 – **T**255:255.PRE/15	
Timer Accumulator Value	**T**f:n.ACC/b	**T**4:0.ACC/0 – **T**4:255.ACC/15, **T**9:0.ACC/0 – **T**255:255.ACC/15	
Counter flag	**C**f:n/b	**C**5:0/0 – **C**5:255/15, **C**9:0/0 – **C**255:255/15	
	Cf:n/CU	**C**5:0/CU – **C**5:255/CU, (b = 15) **C**9:0/CU – **C**255:255/CU (b = 15)	
	Cf:n/CD	**C**5:0/CD – **C**5:255/CD, (b = 14) **C**9:0/CD – **C**255:255/CD (b = 14)	
	Cf:n/DN	**C**5:0/DN – **C**5:255/DN, (b = 13) **C**9:0/DN – **C**255:255/DN (b = 13)	
	Cf:n/OV	**C**5:0/OV – **C**5:255/OV, (b = 12) **C**9:0/OV – **C**255:255/OV (b = 12)	
	Cf:n/UN	**C**5 :0/UN – **C**5 :255/UN, (b = 11) **C**9 :0/UN – **C**255 :255/UN (b = 11)	
	Cf:n/UA	**C**5:0/UA – **C**5:255/UA, (b = 10) **C**9:0/UA – **C**255:255/UA (b = 10)	
Counter	**C**f:n.PRE/b	**C**5:0.PRE/0 – **C**5:255.PRE/15, **C**9:0.PRE/0 – **C**255:255.PRE/15	
Counter Accumulator Value	**C**f:n.ACC/b	**C**5:0.PRE/0 – **C**5:255.PRE/15, **C**9:0.PRE/0 – **C**255:255.PRE/15	
Control	**R**f:n/b	**R**6:0/0 – **R**6:255/15, **R**9:0/0 – **R**255:255/15	
	Rf:n/EN	**R**6:0/EN – **R**6:255/EN, (b = 15) **R**9:0/EN – **R**255:255/EN (b = 15)	
	Rf:n/EU	**R**6 :0/EU – **R**6 :255/EU, (b = 14) **R**9 :0/EU – **R**255 :255/EU (b = 14)	
	Rf:n/DN	**R**6:0/DN – **R**6:255/DN, (b = 13) **R**9:0/DN – **R**255:255/DN (b = 13)	
	Rf:n/EM	**R**6:0/EM – **R**6:255/EM, (b = 12) **R**9:0/EM – **R**255:255/EM (b = 12)	

接点种类	符号格式 Word No. (n) Slot No. (s) File No. (f) Bit No. (b)	读写地址范围	注
Control	**R**f:n/ER	**R**6:0/ER – **R**6:255/ER, (b = 11) **R**9:0/ER – **R**255:255/ER (b = 11)	
	Rf:n/UL	**R**6:0/UL – **R**6:255/UL, (b = 10) **R**9:0/UL – **R**255:255/UL (b = 10)	
	Rf:n/IN	**R**6:0/IN – **R**6:255/IN, (b = 9) **R**9:0/IN – **R**255:255/IN (b = 9)	
	Rf:n/FD	**R**6:0/FD – **R**6:255/FD, (b = 8) **R**9:0/FD – **R**255:255/FD (b = 8)	
Control size of bit array	**R**f:n.LEN/b	**R**6:0.LEN/0 – **R**6:255.LEN/15, **R**9:0.LEN/0 – **R**255:255.LEN/15	
Control Reserved	**R**f:n.POS/b	**R**6:0.POS/0 – **R**6:255.POS/15, **R**9:0.POS/0 – **R**255:255.POS/15	
Integer	**N**f:n/b	**N**7:0/0 – **N**7:255/15, **N**9:0/0 – **N**255:255/15	
Long Word File	**L**f:n/b	**L**9:0/0 – **L**255:255/31	

NOTE

注1　此通信协议仅支持 CRC 错误检查模式。